

**DECLARATION FOR PATENT APPLICATION
AND POWER OF ATTORNEY**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below adjacent to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of subject matter (process, machine, manufacture, or composition of matter, or an improvement thereof) which is claimed and for which a patent is sought by way of the application entitled

**CIRCUIT AND METHOD FOR REDUCING EAST-WEST GEOMETRY MISMATCH
BETWEEN THE TOP AND BOTTOM OF A RASTER DISPLAY**

which (check) ☒ is attached hereto.
☐ and is amended by the Preliminary Amendment attached hereto.
☐ was filed on as Application Serial No.
☐ and was amended on (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information, which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby appoint the following practitioners to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith:

Customer Number 28345



28345

PATENT TRADEMARK OFFICE

Please address all correspondence and telephone calls to:

Todd M. Briggs
SKJERVEN MORRILL MacPHERSON LLP
 Three Embarcadero Center, 28th Floor
 San Francisco, California 94111
 Telephone: 415-217-6000
 Facsimile: 415-434-0646

I declare that all statements made herein of my own knowledge are true, all statements made herein on information and belief are believed to be true, and all statements made herein are made with the knowledge that whoever, in any matter within the jurisdiction of the Patent and Trademark Office, knowingly and willfully falsifies, conceals, or covers up by any trick, scheme, or device a material fact, or makes any false, fictitious or fraudulent statements or representations, or makes or uses any false writing or document knowing the same to contain any false, fictitious or fraudulent statement or entry, shall be subject to the penalties including fine or imprisonment or both as set forth under 18 U.S.C. 1001, and that violations of this paragraph may jeopardize the validity of the application or this document, or the validity or enforceability of any patent, trademark registration, or certificate resulting therefrom.

Full name of sole inventor:

Anatoliy V. Tsyrganovich

Inventor's Signature:

Date: October 24, 2001

Residence:

San Jose, California

Post Office Address:

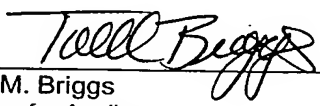
906 Windsor Hill Circle
 San Jose, California 95123

Citizenship: Belarus

REQUEST AND CERTIFICATION UNDER 35 U.S.C. 122(b)(2)(B)(i)	Inventors	Anatoliy V. Tsyrganovich
	Title	Circuit And Method For Reducing East-West Geometry Mismatch Between The Top And Bottom Of A Raster Display
	Atty Docket Number	M-11723 US/ZILG519

I hereby certify that the invention disclosed in the attached application **has not and will not be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. I hereby request that the attached application not be published under 35 U.S.C. 122(b).

October 31, 2001
Date


Todd M. Briggs
Attorney for Applicants
Reg. No.: 44,040

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application **upon filing**.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant **must** notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. **Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).**